

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. P043D2C1)

8 Mackley
3-24-99
7/13

In the Application of:

FARMWALD ET AL.

Serial No: 09/196,200

Filed: NOVEMBER 20, 1998

Title: SYNCHRONOUS MEMORY DEVICE HAVING
A PROGRAMMABLE REGISTER AND
METHOD OF CONTROLLING SAME

Group

Art Unit: 2818

Before

Examiner: T. Nguyen

Assistant Commissioner for Patents
Washington, DC 20231

AMENDMENT

Dear Sir:

In response to the Office Action dated March 17, 1999, I hereby
amend the application as follows:

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1999 MAR 24 PM 2 12
TECHNOLOGY CENTER -2800

IN THE SPECIFICATION:

On page 11, line 14, insert -- Figure 16 is a block diagram
representation of a set of internal registers within each device
illustrated in Figure 2.--

On page 14, line 3, replace "Each" with -- With reference to
Figure 16, each --

On page 14, line 4, after "registers" insert --170--.

On page 14, line 5, after the first occurrence of "register"
insert --171--.

03/25/1999 SHACKNEY 00000003 500238
01 FC:103
02 FC:102

B1
B2
70